

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,107,319 B2  
APPLICATION NO. : 09/871853  
DATED : September 12, 2006  
INVENTOR(S) : Sashikanth Chandrasekaran

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Page 2, Col. 1, Item (56) References Cited

**OTHER PUBLICATIONS**

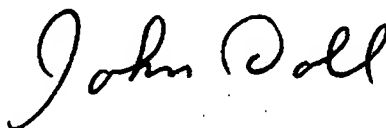
Please insert

--Lory D. Molesky et al., "Database Locking Protocols for Large-Scale Cache-Coherent Shared Memory Multiprocessors: Design, Implementation and Performance," June 6, 1995, pages 1-24.

Lory D. Molesky et al., "Efficient Locking for Shared Memory Database Systems," March 1994, 28 pages.--

Signed and Sealed this

Seventeenth Day of February, 2009



JOHN DOLL  
*Acting Director of the United States Patent and Trademark Office*